# Preliminary Agenda

**TUESDAY, SEPTEMBER 5th**

**REGISTRATION & BREAKFAST**
LE WESTIN MONTREAL, 270 RUE SAINT-ANTOINE O
7:30 AM - 8:30 AM — Breakfast

<table>
<thead>
<tr>
<th>Time</th>
<th>TRAINING</th>
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</thead>
<tbody>
<tr>
<td><strong>8:30 AM - 10:15 AM</strong></td>
<td>Bringing Your Model Into Real Time</td>
<td>The Behind-The-Scenes of Inter-FPGA Communication Using High Speed Serial Links</td>
<td>Using HYPERSIM Advanced Features - Part I</td>
</tr>
<tr>
<td><strong>10:15 AM - 10:35 AM</strong></td>
<td><strong>BREAK</strong></td>
<td><strong>BREAK</strong></td>
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<tr>
<td><strong>10:35 AM - 12:00 PM</strong></td>
<td>Achieving Test Automation With eMEGASIM</td>
<td>How to Use State Space Nodal Efficiently</td>
<td>Using HYPERSIM Advanced Features - Part II</td>
</tr>
<tr>
<td><strong>12:00 PM - 1:00 PM</strong></td>
<td><strong>LUNCH</strong></td>
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<tr>
<td><strong>1:00 PM - 2:45 PM</strong></td>
<td>Large Grid Model Import Made Easy</td>
<td>A New Way of Interacting with Your RT-LAB Model</td>
<td>Power System Protection, Control and Monitoring Applications - Part I</td>
</tr>
<tr>
<td><strong>2:45 PM - 3:05 PM</strong></td>
<td><strong>BREAK</strong></td>
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<tr>
<td><strong>3:05 PM - 4:30 PM</strong></td>
<td>ePHASORSIM: User Defined Modeling</td>
<td>Integrating your Controller with our Multi-Level Modular Converter (MMC)</td>
<td>Power System Protection, Control and Monitoring Applications - Part II</td>
</tr>
</tbody>
</table>

**REGISTRATION**
4:00 PM - 6:00 PM — Registration period

**WELCOME COCKTAIL**
SALLE DE BAL WESTIN
6:00 AM - 10:00 AM — WELCOME COCKTAIL
Evening sponsored by tribase

If you haven’t reserved your room at Le Westin Montreal yet, do so now and take advantage of our group rate!

**WWW.OPAL-RT.COM/RT17**

* (schedule subject to change)
**WEDNESDAY, SEPTEMBER 6th**

**PRESENTATIONS**

- **7:15 AM - 8:30 AM**
  - Welcome to RT17!

- **8:30 AM - 9:15 AM**
  - Power Systems Keynote by Etienne Leclerc, OPAL-RT

- **9:15 AM - 10:40 AM**
  - Power Systems Keynote by Etienne Leclerc, OPAL-RT

- **10:40 AM - 11:10 AM**
  - Digital Transformation - Disrupt or Be Disrupted!

- **11:10 AM - 12:00 PM**
  - Power Systems Keynote by Denis Gaudreault, Intel, Canada

- **12:00 PM - 1:00 PM**
  - LUNCH

**TECHNICAL PRESENTATIONS**

<table>
<thead>
<tr>
<th>TRACK 1 - MICROGRID</th>
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<tbody>
<tr>
<td>HD Experiences in Case of Using Control System Replica Interfaced WithYPHESIM Real-Time Simulator in Recent HVDC Upgrade Projects by Alpha Duan, BCEI, Canada</td>
<td>New Switched-DC Cascades Multilevel Inverter for Distributed DC Sources by Christoph Luciani, Texas A&amp;M University, USA</td>
<td>High-Fidelity Power Motor Emulator for Testing Inverter and Control by Desty Nazarchuk, OPAL-RT Technologies, Canada</td>
</tr>
<tr>
<td>Real-Time Simulation of Predictive Control of DC Vehicular Microgrids by All Mehrizi-Sani, Washington State University, USA</td>
<td>The Use of Real-Time Simulation to De-risk and Manage HVDC and FACTS Schemes - Experiences on the French Transmission Grid by Jean-Marc Martin, RTE, France</td>
<td>Real-Time Control of Induction Motor with Rapid Control Prototyping using RT-LAB Software, by Puja Deepak, IIT Guwahati, India</td>
</tr>
<tr>
<td>Real-Time Co-Simulation for Microgrids With OPAL-RT by Quoc Vu Quyen, CEA-INES, France</td>
<td>New Travelling Wave Fault Location at SEL and the Need for Advanced HIL Solutions, by Dr. Rajkumar Vasanathan, SEL Inc., Canada</td>
<td>Design and Implementation of a Modular Multilevel Converter Supported by HIL Simulation by Francois Dussault, 12RP, France</td>
</tr>
<tr>
<td>Real-Time Hardware-in-the-Loop Co-Simulation Platform for Microgrid Analysis by Martin Tegge, McGill University, Canada</td>
<td>How to Use Real-Time Simulation for a Better, Modern and Interactive Teaching Experience for Power System by Desty Nazarchuk, OPAL-RT TECHNOLOGIES, Canada</td>
<td>HIL Implementation of Model Predictive Current Control Algorithm for Power Converter Using OPCUa and OPAL-RT by Nikhil Pagi, India</td>
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**COCKTAIL AND DINNER**

**SALLE DE BAL WESTIN**

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**TERRACE OF THE PALACE DES CONGRES DE MONTREAL**

**CLOSING CEREMONY AND OPAL-RT’S 20TH ANNIVERSARY CELEBRATION!**

**Thursday, September 7th**

**PRESENTATIONS**

- **7:15 AM - 8:30 AM**
  - Breakfast

- **8:30 AM - 9:00 AM**
  - Power Electronics and Power-Hardware-in-the-Loop Keynote by Christophe Brayer, OPAL-RT

- **9:00 AM - 10:00 AM**
  - Power Electronics and OPAL-RT Keynote by Denis Gaudreault, Intel, Canada

- **10:00 AM - 10:30 AM**
  - Innovating in a IoT, IP World by Greg Fanning, ABB, Canada

- **10:30 AM - 11:00 AM**
  - Automotive Keynote by Herve Pollart, OPAL-RT

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  - Aerospace & Defense Keynote by Alexandre Leboeuf, OPAL-RT

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<td>Vehicle-Grid Integration HIL for Designing Advanced Ancillary Services for Power Systems by Tatsuo Ota, Tokyo City University, Japan</td>
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<td>Power-HIL and the IOT Energy Smart Home Laboratory by Sebastian Ruhlschneider, Karlsruhe Institute of Technology, Germany</td>
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**FRIDAY, SEPTEMBER 8th**

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<td>Buses drive out, end of RT17</td>
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**VISITS (GROUP 1)**

Pick-up from Le Westin at 8AM

**VISITS (GROUP 2)**

Pick-up from Le Westin at 9AM

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